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- ☐ 1. **IEEE 1394a_2000 physical layer ASIC**
Yashwante, R.; Jahagirdar, B.;
[Design Automation Conference, 2002. Proceedings of ASP-DAC 2002. 7th Asia and South Pacific and the 15th International Conference on VLSI Design. Proceedings.](#)
7-11 Jan. 2002 Page(s):795 - 800
Digital Object Identifier 10.1109/ASPDAC.2002.995030
[AbstractPlus](#) | Full Text: [PDF](#)(299 KB) IEEE CNF
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- ☐ 2. **Smart gateway systems for Internet security for broadband communication networks: SoC solutions and FPGA demonstrations**
Jing-Long Chu; Guan Yu; Chong, E.; Chia-Chi Chu;
[ASIC, 2003. Proceedings. 5th International Conference on](#)
Volume 2, 21-24 Oct. 2003 Page(s):1317 - 1320 Vol.2
Digital Object Identifier 10.1109/ICASIC.2003.1277459
[AbstractPlus](#) | Full Text: [PDF](#)(406 KB) IEEE CNF
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- ☐ 3. **Minimizing area and maximizing porosity for cell layouts using innovative routing strategies**
Sengupta, S.; Ramanathan, S.; Chatterjee, B.; Goswami, D.;
[VLSI Design, 2001. Fourteenth International Conference on](#)
3-7 Jan. 2001 Page(s):353 - 358
Digital Object Identifier 10.1109/ICVD.2001.902684
[AbstractPlus](#) | Full Text: [PDF](#)(448 KB) IEEE CNF
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- ☐ 4. **Design and implementation of a multicomputer interconnection network using FPGAs**
Chun-Chao Yeh; Chun-Hsing Wu; Jie-Yong Juang;
[FPGAs for Custom Computing Machines, 1995. Proceedings. IEEE Symposium on](#)
19-21 April 1995 Page(s):56 - 60
Digital Object Identifier 10.1109/FPGA.1995.477409
[AbstractPlus](#) | Full Text: [PDF](#)(384 KB) IEEE CNF
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- ☐ 5. **The ATM layer chip: an ASIC for B-ISDN applications**
Johnston, C.A.; Chao, H.J.;
[Selected Areas in Communications, IEEE Journal on](#)
Volume 9, Issue 5, June 1991 Page(s):741 - 750
Digital Object Identifier 10.1109/49.87644
[AbstractPlus](#) | Full Text: [PDF](#)(848 KB) IEEE JNL
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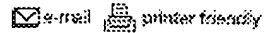
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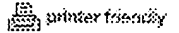
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- ☐ 1. **A QoS-aware, energy-efficient wireless node architecture**
Lettieri, P.; Srivastava, M.B.;
Mobile Multimedia Communications, 1999. (MoMuC '99) 1999 IEEE International Workshop on
15-17 Nov. 1999 Page(s):252 - 261
Digital Object Identifier 10.1109/MOMUC.1999.819498

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 - ☐ 2. **Highly concurrent shared storage**
Amiri, K.; Gibson, G.A.; Golding, R.;
Distributed Computing Systems, 2000. Proceedings. 20th International Conference on
10-13 April 2000 Page(s):298 - 307
Digital Object Identifier 10.1109/ICDCS.2000.840942

AbstractPlus | Full Text: PDF(88 KB) **IEEE CNF**
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 - ☐ 3. **A personal computer approach to overcurrent protective device coordination**
Brown, K.A.; Parker, J.M.;
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Volume 3, Issue 2, April 1988 Page(s):509 - 513
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 - ☐ 4. **Man-computer graphics for computer-aided design**
Prince, M.D.;
Proceedings of the IEEE
Volume 54, Issue 12, Dec. 1966 Page(s):1698 - 1708

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 - ☐ 5. **Mass storage: A broad review**
Bonn, T.H.;
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Volume 54, Issue 12, Dec. 1966 Page(s):1861 - 1870

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 - ☐ 6. **FISHNET: a distributed architecture for high-performance local computer networks**
Kang, Y.J.; Herzog, J.H.; Spragins, J.;
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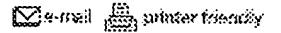
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Lee, C.L.;
[VLSI Design, Automation and Test, 2005. \(VLSI-TSA-DAT\). 2005 IEEE VLSI-TSA International Symposium on](#)
27-29 April 2005 Page(s):140 - 143
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- ☐ 2. Interesting applications of Atmel AVR microcontrollers
Korbel, S.; Janes, V.;
[Digital System Design, 2004. DSD 2004. Euromicro Symposium on](#)
31 Aug.-3 Sept. 2004 Page(s):499 - 506
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- ☐ 3. A dice game in third person augmented reality
Colvin, R.; Hung, T.; Jimison, D.; Johnson, B.; Myers, E.; Blaine, T.;
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7 Oct. 2003 Page(s):3 - 4
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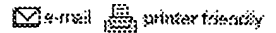
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- ☐ 1. **QoS support for USB 2.0 periodic and sporadic device requests**
Chih-Yuan Huang; Tei-Wei Kuo; Ai-Chun Pang;
[Real-Time Systems Symposium, 2004. Proceedings. 25th IEEE International 5-8 Dec. 2004 Page\(s\):395 - 404](#)
Digital Object Identifier 10.1109/REAL.2004.45
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- ☐ 2. **A cyclic-executive-based QoS guarantee over USB**
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[Real-Time and Embedded Technology and Applications Symposium, 2003. Proceedings. The 9th IEEE 27-30 May 2003 Page\(s\):88 - 95](#)
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- ☐ 3. **USB on-the-go interface for portable devices**
Remple, T.B.;
[Consumer Electronics, 2003. ICCE. 2003 IEEE International Conference on 17-19 June 2003 Page\(s\):8 - 9](#)
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- ☐ 4. **Handheld haptics: a USB media controller with force sensing**
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[Haptic Interfaces for Virtual Environment and Teleoperator Systems, 2002. HAPTICS 2002. Proceedings. 10th Symposium on 24-25 March 2002 Page\(s\):311 - 318](#)
Digital Object Identifier 10.1109/HAPTIC.2002.998974
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- ☐ 1. **A single-chip storage LSI for home networks**
 Han-Kyu Lim; Deog Kyoon Jeong; KyungTae Kim; JunMo Park; Han-gyoo Kim;
[Communications Magazine, IEEE](#)
 Volume 43, Issue 5, May 2005 Page(s):141 - 148
 Digital Object Identifier 10.1109/MCOM.2005.1453436
Summary: Network direct attached storage (NDAS) is a network storage architecture that allows direct attachment of existing ATA/ATAPI devices to Ethernet without a separate server. Unlike other architectures such as NAS, SAN, and USB mass storage, no server c.....
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- ☐ 2. **Development and evaluation of a CMOS sensor-based digital intra-oral radiographic system**
 Soonil Hong; Haijo Jung; Kee-Deog Kim; Su-Gil So; Jong-Ho Kim; Sun Kook Yoo; Hyung Sik Yoo; Hee-Joung Kim;
[Nuclear Science, IEEE Transactions on](#)
 Volume 52, Issue 1, Part 1, Feb. 2005 Page(s):256 - 261
 Digital Object Identifier 10.1109/TNS.2004.843167
Summary: As part of an ongoing investigation into the development of a dental digital radiographic (DDR) system using a complementary metal-oxide semiconductor (CMOS) sensor, this study developed hardware and software with a graphical user interface to acquire.....
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- ☐ 3. **Telos: enabling ultra-low power wireless research**
 Polastre, J.; Szewczyk, R.; Culler, D.;
[Information Processing in Sensor Networks, 2005. IPSN 2005. Fourth International Symposium on](#)
 15 April 2005 Page(s):364 - 369
 Digital Object Identifier 10.1109/IPSN.2005.1440950
Summary: We present Telos, an ultra low power wireless sensor module ("mote") for research and experimentation. Telos is the latest in a line of motes developed by UC Berkeley to enable wireless sensor network (WSN) research. It is a new mote design built from.....
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- ☐ 4. **Implementation of EIDE disk array system for mass data backup**
 Kun Gao; Jing Pei; Haizheng Xu; Longfa Pan;
[Aerospace and Electronic Systems Magazine, IEEE](#)
 Volume 19, Issue 11, Nov. 2004 Page(s):24 - 29
 Digital Object Identifier 10.1109/MAES.2004.1365662
Summary: Today, EIDE hard disk drives have become the mainstream products in low-end storage applications. An inexpensive disk array system based on EIDE hard disk drives is